

P-Type-M10-182.2x182.2 Φ 247-0.4~1.1-Grade-A



High conversion efficiency with high reliability



No light-induced degradation



Uniform cell performance with stable process control



Both sides can generate electricity



Low mismatch of cell performance during encapsulation



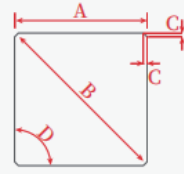
Excellent power generation performance under low irradiation



Low hot spot effect

SOLARIS PANABO

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Property	Specification	Inspection Method
Conducting Type	P-Type	PN-type tester(DLY-2 P/N)
Doping Element	Ga.	-
Resistivity	0.4-1.1	Silicon wafer automatic detection equipment
Minority Carrier Lifetime	≥ 70	Quasi steady state photoconductivity decay method transient photoconductivity decay method
Gap Oxygen Content	$\leq 7.52 \times 10^{17}$	Fourier transform infrared spectrometer
Substitute Carbon Content	$\leq 5.0 \times 10^{16}$	Fourier transform infrared spectrometer
Growth Mode	Straight pull method	-
Crystallinity	Monocrystalline	-
Dislocation Density	≤ 500	Optimal chemical corrosion method
Surface Crystal Orientation	$\langle 100 \rangle \pm 2$	X-ray diffractometer
Lateral Crystal Orientation	$\langle 010 \rangle, \langle 001 \rangle \pm 2$	X-ray diffractometer
Silicon Wafer Model	M10	-
Geometric Shape	Quasi-square	-
Chamfered Edge Shape	Arc	-
Margins	182.2 ± 0.25	Silicon wafer automatic detection equipment
Diagonal After Chamfering	$\Phi 247 \pm 0.25$	Silicon wafer automatic detection equipment
Chamfering Projection	7.72 ± 0.25	Silicon wafer automatic detection equipment
Verticality	90 ± 0.15	Silicon wafer automatic detection equipment
Thickness	150 ± 10	Silicon wafer automatic detection equipment
TTV	≤ 25	Silicon wafer automatic detection equipment
Line Mark	≤ 13	Silicon wafer automatic detection equipment
Curvature	≤ 40	Silicon wafer automatic detection equipment
Warpage	≤ 40	Silicon wafer automatic detection equipment
Cutting Method	Diamond wire cutting	Silicon wafer automatic detection equipment
Surface Quality	Clean surface, no visible pollution, color difference (judged according to standard samples) (No oil stains, fingerprints, flower spots, mortar residue, adhesive residue allowed)	
Edge Chipping	Depth $\leq 0.3\text{mm}$ &length $\leq 0.5\text{mm}$, each piece cannot exceed one, with no V-shaped broken edges	Manual inspection or automatic silicon wafer testing equipment
Hidden Cracks/Pores	Not allowed	Silicon wafer automatic detection equipment
Schematic Diagram of Silicon Wafer Size	 Size: M10 A: $182.2 \pm 0.25\text{mm}$ B: $\Phi 247 \pm 0.25\text{mm}$ C: $7.72 \pm 0.50\text{mm}$ D: $90 \pm 0.15^\circ$	